

Sheet1

STLCAM V1.1 pin mappings

1	58 PO_SYNC_IN	13	55 PO_SYNC_OUT	1	1/2/33/49/50/51	11	1/2/33/49/50/51	1	1/2/33/49/50/51	13	3 MII_CRS
2	63 PO_U0TX_CANTX	14	62 PO_U0TX_CANRX	2	25 JTRST	12	GND	2	4 MII_TXD2	14	5 MII_TXD3
3	42 PO_U1TX_CANTX	15	43 PO_U1TX_CANRX	3	26 JTDI	13	GND	3	6 MII_TXD0	15	7 MII_TXD1
4	36 P0_SSP0_SCLK	16	37 P0_SSP0_MOSI	4	27 JTMS	14	GND	4	9 MII_TXCLK	16	10 MII_TXEN
5	38 P0_SSP0_MISO	17	39 P0_SSP0_NSS	5	28 JTCK	15	GND	5	11 MII_RXER	17	12 MII_CLK
6	44 P0_SSP1_SCLK	18	45 P0_SSP1_MISO	6		16	GND	6	13 MII_RXDV	18	14 MI_RXCLK
7	46 P0_SSP1_MOSI	19	47 P0_SSP1_NSS	7		17	GND	7	15 MII_RXD1	19	18 MII_RXD0
8	53 PO_ADC0	20	54 PO_ADC1	8	31 RESET_IN	18	GND	8	19 MII_RXD3	20	20 MII_RXD2
9	41 PO_DMA_REQ1	21	57 PO_ADC3	9		19	GND	9	21 MII_MDIO	21	22 MII_MDC
10	GND	22	GND	10		20	GND	10	GND	22	GND
11		23	1/2/33/49/50/51					11		23	1/2/33/49/50/51
12	GND	24	GND					12	GND	24	GND

1 3V3\_CAM  
 2 MII\_COL  
 33 3V3\_RAM  
 49 3V3\_uC  
 50 V\_BATT  
 51 V\_R\_A

The defitions of the pins can be found in the  
[http://airwiki.ws.dei.polimi.it/images/f/f6/RVS\\_Datasheet\\_v2.1.pdf](http://airwiki.ws.dei.polimi.it/images/f/f6/RVS_Datasheet_v2.1.pdf)